

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

2. Authorization for this examiner's amendment was given in a telephone interview with Jun Ha on 6/26/2008.

3. The application has been amended as follows:

Please amend the claims as follows:

Claim 1, line 6 from "received from the optical signal receiver into a parallel data" to – received from the optical signal receiver into 10 bit parallel data –

Claim 5, the last line from "resolution information and the clock signal" to – resolution information and the horizontal synchronous signal. –

Claim 8, line 6, from "generator 420 according to the counted number of clock signals according to the resolution" to – generator according to the counted number of clock signals according to the resolution –

Claim 11, line 6 from "received from the optical signal receiver into a parallel data" to – received from the optical signal receiver into 10 bit parallel data –

Claim 11, line 7, change "a 10 bit parallel data" to – the 10 bit parallel data --

Claim 11, line 9, change "a second image signal processor" to -- an image signal processor –

Claim 11, line 13, change “a second clock signal generator” to – a clock signal generator –

Claim 11, line 15, change from “second image signal processor” to -- image signal processor –

Claim 11, line 16, change “the second clock signal generator” to –the clock signal generator --.

Claim 11, line 19, change “the second image signal processor” to – the image signal processor --.

Claim 11, line 22, change “the second image signal processor” to – the image signal processor --.

Claim 11, line 25, change “the second image signal processor” to – the image signal processor --.

Claim 15, line 12, change “the clock signal count number (N0” to – the clock signal count number (N) –

Claim 19, line 1, change “the count number (a)” to – the count number (A) –

Please amend the specification as follows:

Page 13, line 11 (published as paragraph 61), should be changed from “by using the vertical active signal, the resolution information and the clock signal” to – by using the vertical active signal, the resolution information and the horizontal synchronous signal. --

This change is made so the specification and claims are in agreement.

4. The following is an examiner's statement of reasons for allowance:

The present invention is directed to an apparatus for restoring synchronous and active signals in a display unit receiving data through an optical signal receiving unit. Independent claim 1 identifies the uniquely distinct features of using resolution information from an image signal packet to set a clock frequency and using the generated clock frequency along with resolution information and other signals for restoring the horizontal and vertical synchronous and active signals. Independent claim 11 identifies the uniquely distinct features of using resolution information from an image signal packet to set a clock frequency and setting a maximum count value according to the resolution information and altering the maximum count value according to a counter output within a period of time based on information in a header of an image signal packet. The closest prior art, Ryu (USPN: 5402243) and Shinohara (USPN: 6977677) disclose methods of horizontal and vertical synchronization pulse restoration, either singularly or in combination, fail to anticipate or render the above underlined limitations obvious.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Steven E. Holton whose telephone number is (571)272-7903. The examiner can normally be reached on M-F 8:30-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala can be reached on (571) 272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Steven E. Holton

Division 2629

June 23, 2008

/Bipin Shalwala/

Supervisory Patent Examiner, Art Unit 2629